## 12/06/2015

QP Code: 3483

		(3 Hours)	[ Total Marks :80
N.I		<ol> <li>Questions No. 1 is compulsory.</li> <li>Attempt any three out of remaining questions.</li> <li>Figure to right indicates full marks.</li> <li>Assume suitable data if necessary.</li> </ol>	CK OF EDICO
1. S	(a) (b) (c) (d) (e)	any five Draw and explain labelled V-I Characteristics of Zener dioc Classify single phase controlled rectifier. State important features of op-amp Realize basic gates using NAND gate Explain back EMF in DC motor. Draw generalized architecture of microcontroller	20
2.	(a)	Draw and explain single phase full bridge controlled rectifie	
	(1.)	of waveforms for R-load, Derive the output voltage equation of MSP420	
	, ,	Explain architecture of MSP430	7
	(c)	Compare R & R C triggering methods of SCR.	6
3	(b)	Explain IC555 as monaostable multivibrator. Compare CMOS and TTL logic family Draw and explain torque-speed characteristics of DC series motor, Also state application of each.	and DC shunt 6
4	(a)	Explain first order filter circuit.	7
	. ,	Explain various registers used for digital I/O of MSP430	7
	(c)	Analyse torque-speed characteristics of induction motor. State v of speed control of induction motor.	
5	(a)	What is commutation of SCR? Explain any one method in de-	tail 7
		Explain closed loop speed control of DC motor, What is the	
	(c)	inner current loop What is decoder, Demultipexer and flip-flop	6
6		Explain suitability of different electric motors for various industric Explain with appropriate waveforms the operation of single	1 1 1 1 1 1 1
(	5	inverter circuit.	e phase bridge 7
4	(c)	Explain different peripherals of MSP430, Why is it called a	s mixed signal 6

JP-Con. 12679-15.

processor?

Correctioned

Course: S.E.(SEM. IV)(CBSGS)(MECHANICAL) CW

(AUTOMOBILE)(PROG 791 TO 805)

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Correction:

Q. No.(3)(a)

Read AS:

## monostable

Instead of:

Monaostable

Query Update time: 12/06/2015 02:50 PM