

SE - Sem - III - Old - Computers

COA

11/6/15

(OLD COURSE)

QP Code : 4611

(3 Hours)

[Total Marks : 100

- N.B. :** (1) Question No. 1 is **compulsory**.
(2) Attempt any **four** questions out of the remaining **six** questions.
(3) Assume suitable **data** wherever **required**.
(4) **Figures** to the **right** indicate **full marks**.

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|--------|--|----|
| 1. (a) | Explain Booth's Algorithm with example. | 10 |
| (b) | Explain the Von Neumann architecture with the help of diagram. | 10 |
| 2. (a) | Explain the interleaved memory in detail. | 10 |
| (b) | Explain different mapping technique of Cache Memory. | 10 |
| 3. (a) | Compare and explain static and dynamic data flow computers. | 10 |
| (b) | Explain SPARC processor in detail. | 10 |
| 4. (a) | Explain with neat diagram systolic processor. | 10 |
| (b) | Explain design of control unit with respect to softwired and hardwired approach. | 10 |
| 5. (a) | Explain IEEE 754 formats. Explain with example. | 10 |
| (b) | Explain different instruction formats with suitable example. | 10 |
| 6. (a) | List and explain the characteristics of memory. | 10 |
| (b) | List and explain various page replacement techniques. | 10 |
| 7. | Solve any two. | 20 |
| (a) | Comparison of RISC and CISC | |
| (b) | RAID | |
| (c) | Difference between SRAM & DRAM | |
| (d) | Wave front Array. | |