TE-SeronV-OLD-CE

PE

11/12/15 Q.P. Code: **1785**

(3	Hours)	
(-		

[Total Marks: 100

		어림은 그는 그리다. 그리고 아이들은 아이들은 그리고 그는 이번 바람이 되었다. 그는 그는 그는 그는 그는 그는 그는 그는 그는 그를 가지 않는 것이다.	
N	.B. :	(1) Questions No.1 is compulsory.	
		(2) Solve any four questions out of remaining six questions.	
		(3) Each question is for 20 marks.	
		(4) Assume suitable data if necessary.	
			9
1	. S	olve the following questions:	20
		(a) Write short note on thyristors family.	
		(b) Explain the principle of operation- of power MOSFET.	
		(c) Draw the circuit diagram of any one application using Triac & Diac and	
		compare their V-1 characteristics.	
		(d) Why there is a restriction on the firing angle in case of ac voltage controllers	
		feeding highly inductive load.	
^			
2.	. (a)	Explain steady state characteristics of common emitter configuration of BJT in details.	10
	(b)	Explain protection circuit of SCR.	10
3.	(a)	Explain in detail the different methods of turning on SCR.	10
	(b)		10
		Ω . The input voltage is $V_s = 120 \text{ V(rms)}$, 60Hz. Determine a) the load current I_{to}	
		at wt = α = 60°, b) the average thyristors current, c) the rms thyristors current, d)	
		the rms output current, e) the average output current.	
4.		Explain with circuit diagram and waveforms, a 1 phase dual converter.	10
	(b)	A 3 phase half wave converter is operated from a 3 phase star connected 208 V	10
		(line to line), 60 Hz supply and load resistance is $R = 10\Omega$. If it is required to	
		obtain an average output voltage 50% of the maximum possible output voltage,	
		calculate a) the delay angle α , b) the rms and average output currents, c) the	
		average and rms thyristor currents, d) the rectification efficiency, e) the TUF, and f) the input power factor.	
		t) the liput power factor.	
5	(a)	Explain with circuit diagram and waveforms 2 above boils in a case	
٥.	(4)	Explain with circuit diagram and waveforms, 3 phase bridge inverter for 120° conduction mode.	10
	(b)	Explain in detail with circuit diagram and waveforms, three phase cyclo-converter.	10
	(-)		10

TURN OVER

20

6. (a)	Explain with circuit diagram and waveforms the step-up converter with L load.	10
(b)	Buck regulator has an input voltage of 12V and the required average output voltage	10
	is 5V. The peak to peak output ripple voltage is 20mV, load resistance is 100 ohms	~
	and the switching frequency is 25kHz. Find a) Duty cycle b) Value of L & C and	0
	c) Average input current, if the peak to peak ripple current in inductor is limited to	
	be 0.8A.	

- 7. Write short note on any two:
 - (a) Four Quadrant operation of Triac.
 - (b) Turn on & Turn off characteristics of SCR.
 - (c) Single phase Dual converter.
 - (d) R & R-C firing circuit of SCR.