

Page No.

Date: / /

* Firing angle control:

- The operation of CC & CEA controller is closely related to gate pulse of valves
- Two types of firing controls
 - (1) Individual phase control (IPC) [used in past]
 - (2) Equidistant pulse control (EPC)

• Individual phase control (IPC):

- Firing instance is determined individually for each valve i.e. phase position of each control pulses is determined separately for each valve and related to commutation voltage (zero crossing)
- Six parallel delay circuit is required.
- For eg; valves 1, 2, 3... will be fired at different angles $\alpha_1, \alpha_2, \alpha_3, \dots$

Note: In equidistant pulse control (EPC), one valve will be fired at an instant say ' α ' & subsequent valves will be fired after 60° .

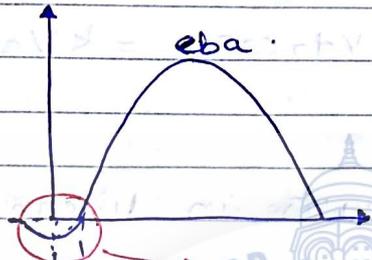
eg:-

Valve 1	→	5°
2	→	65°
3	→	125°
		\vdots
		\vdots

- Hence, there is a symmetry in pattern of o/p voltage which is not the case with IPC.

- Because the O/P Voltage of IPC control is not symmetrical, it involves alot of harmonics.

- Since IPC is related to firing at commutation voltage zero crossing the following can happen.



due to some fault, zero crossing changes, hence changing ' α ' & causing error.

- For IPC since we are calculating from α to α_6 , we require the individual phase delay cts.

However in EPC, we need one ' α ' control & can have a sing counter which gives 60° delay counter is a voltage control oscillator ckt.

once pulse is given, '6' displaced cts will be available which can be given to the phases individually.

- Two types of control measures are there for IPC.

- Cosine control
- Linear Control.

Page No.

Date: / /

- Cosine control.

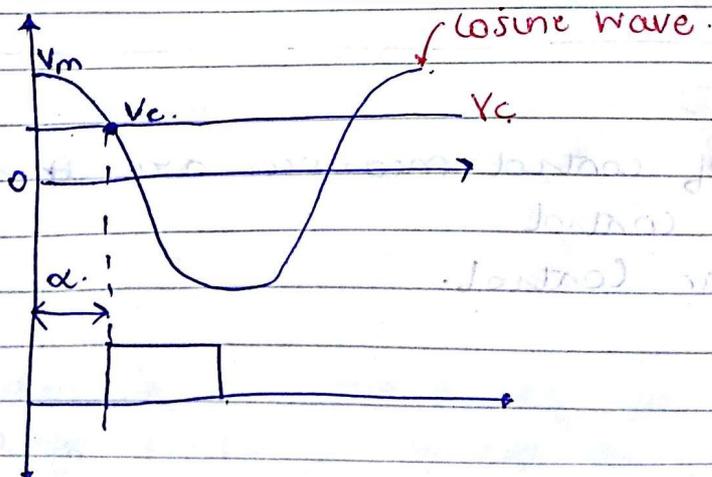
- There are several versions of this method.
- Pulse are generated at the crossing of control voltage (V_c) & ac line voltage.

$$\alpha = \cos^{-1} \left(\frac{V_c}{V_m} \right) ; V_d = V_{d0} \cos \alpha = k V_c.$$

- The control system results in linear transfer characteristic.
- To understand this method we need to understand what is control voltage (V_c).



From the above it's clear that the control voltage is derived from the error signal through some block (circuit).



Page No.

Date: / /

- From above it is clear that the ' V_c ' is compared with cosine wave ^{& when the two are equal a pulse is generated.} to get a point which indicates the firing instant of valves.

- So we have;

$$V_c = V_m \cos \alpha$$

$$\text{or } \alpha = \cos^{-1} \left(\frac{V_c}{V_m} \right)$$

- This cosine control is very advantageous, because we know,

$$V_d = V_{do} \cos \alpha$$

$$= V_{do} \left(\frac{V_c}{V_m} \right)$$

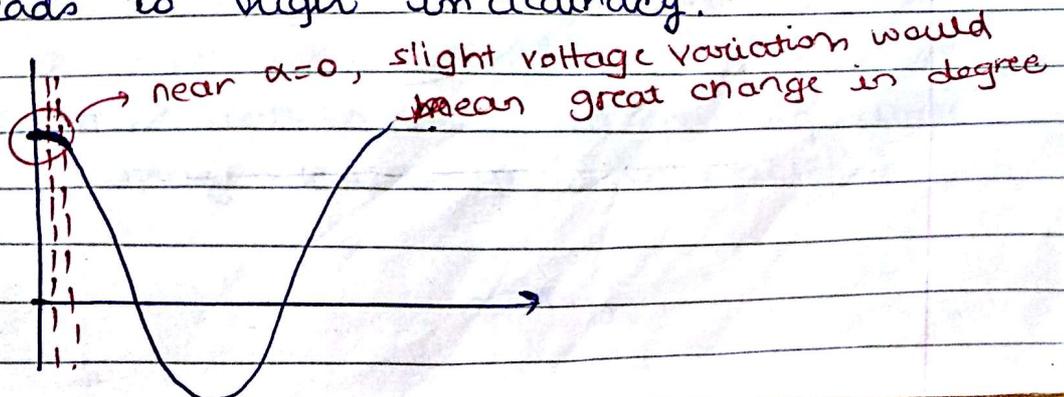
$$= V_{do} \cdot \frac{V_c}{V_m}$$

$$V_d = k V_c$$

→ o/p voltage is proportional to control voltage.

- The o/p voltage is independent on change in input ac voltage.

- However near alpha zero, it is very sensitive to V_c & leads to high inaccuracy.

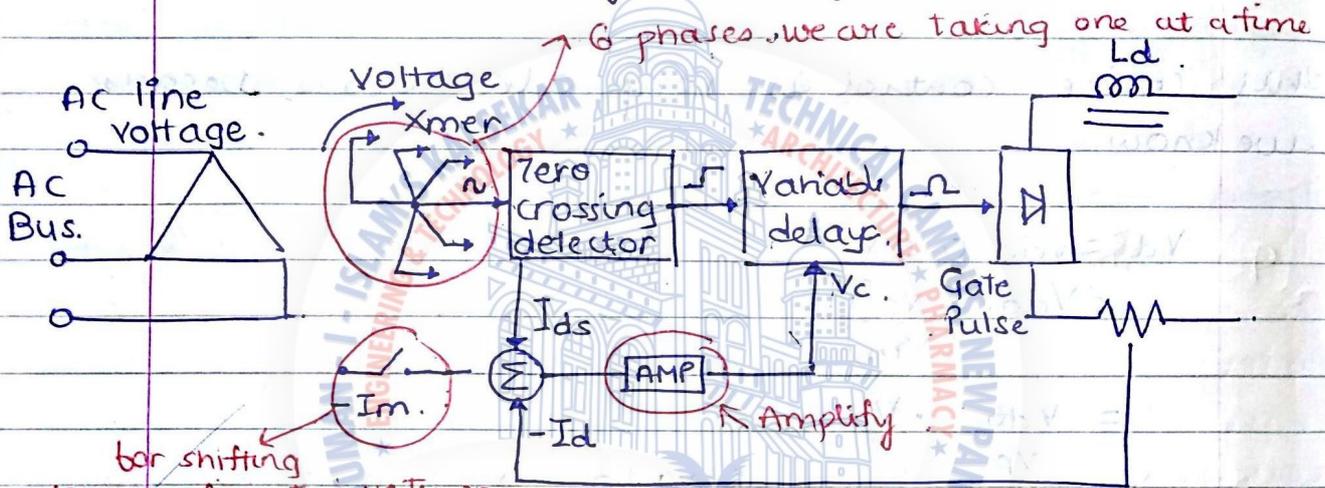


• Linear Control.

- Pulses are generated at the crossing of control voltage (V_c) & ac line voltage.

$$\alpha = k_1 V_c \quad \therefore V_d = V_{d0} \cos(k_1 V_c)$$

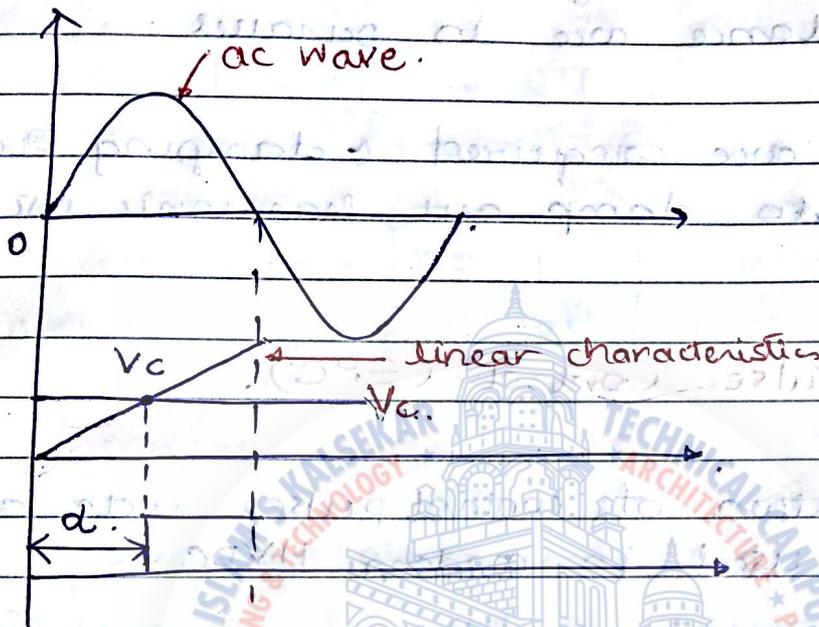
- This makes linear transfer characteristics non-linear. But ~~accuracy~~ error is of $\pm 1^\circ$.



• Advantages of IPC:

- DC %P voltage is higher.

Note: Linear control has the following waveform.



- Once zero crossing of ac wave is detected, a linear generator is used to obtain linear relationship & wherever ' V_c ' intersects linear characteristic, the firing pulse is generated.

$$\therefore V_c = k \alpha.$$

slope [some constant value]

Disadvantages of IPC:

- (1) Due to fault, distortion etc, zero crossing will shift & this may result in uncharacteristic harmonics

Page No.

Date: / /

(2) May cause harmonic ^{instability.} insensitivities (weak ac systems) at frequencies where filter impedance & system impedance are in parallel.

(3) More filters are required & damping resistance is required to damp out harmonic oscillations.

• Equidistant Pulse Control (EPC).

- No synchronisation of control pulses with applied ac voltage & used in modern HVDC.

- It produces pulses at intervals of ~~1/fp~~ $\left[\frac{1}{fp} \right]$.

- There are 2 methods of EPC.

• Pulse Frequency Control (PFC)

• Pulse Phase Control (PPC)

- The method gives low D.C. O/P voltage but is successful in weak ac systems

- EPC scheme also results in high negative damping contribution to torsional oscillations

$$P_d = (V_d)(I_d) \quad / \quad P_d = (V_d)(-I_d) \quad /$$

$$P_d = (-V_d)(I_d) \quad / \quad P_d = (V_d)(-I_d) \quad /$$

$$P_d = V_d I_d \quad -$$

$$P_d = -V_d I_d \quad -$$

duration of
↑
each pulse.

Page No. _____

Date: / /

→ we know that $\omega t = \frac{2\pi}{P}$

$$\therefore t = \frac{2\pi}{\omega P} = \frac{2\pi}{2\pi f_o P}$$

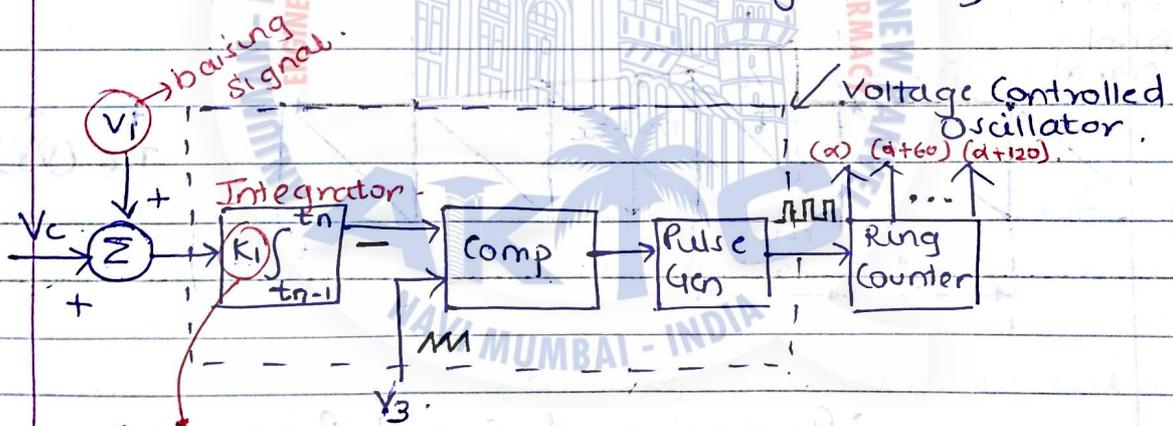
360° divided by
no. of pulses
gives us ωt .

$$t = \frac{1}{f_o P}$$

→ nominal
frequency (50Hz)

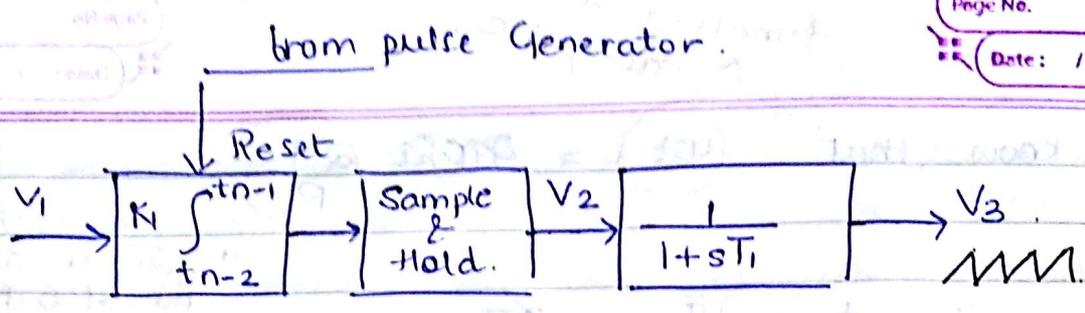
• Pulse Frequency Control (PFC).

- The frequency of voltage control oscillator (VCO) is determined by control voltage (V_c) related to the error in current, gamma, or voltage.



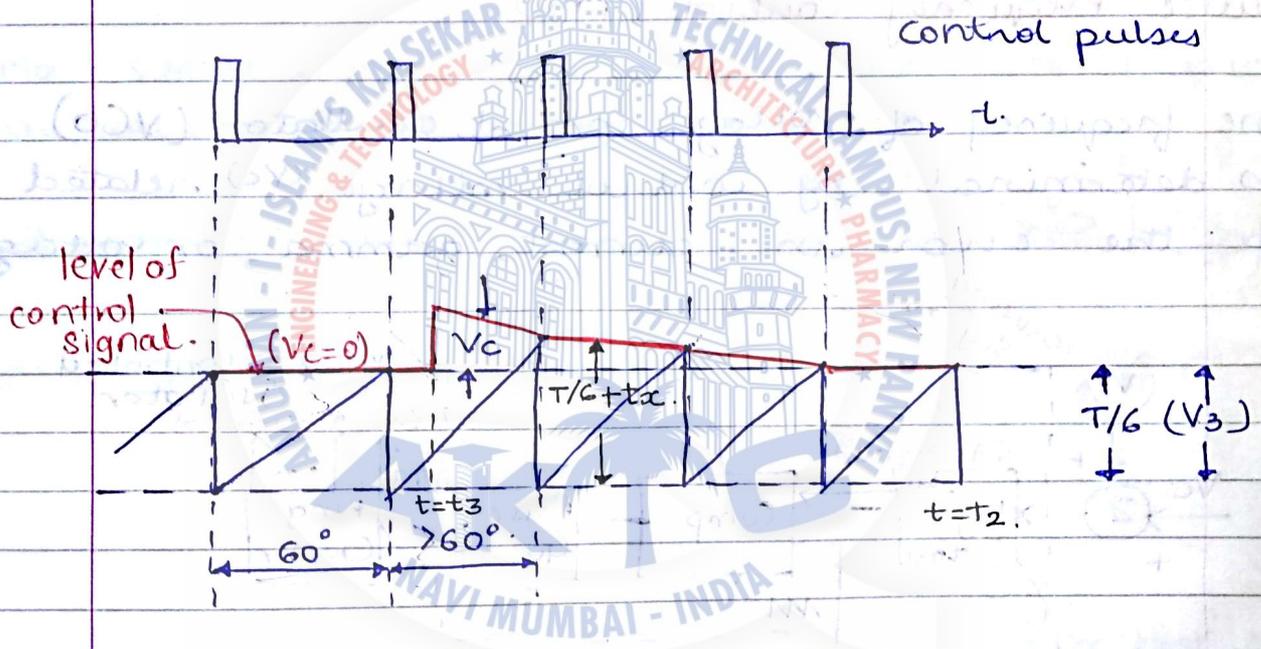
Gain to stabilize it • Block diagram of PFC system

- Biasing signal should be greater than zero to make entire system stable.
- Control voltage (generated due to error in current/voltage) is added to the biasing signal & integrated.



• Frequency correction for PFC-

- If %P of integrator & V3 match, pulse is generated



• Generation of Control Pulses.

$$- k_i \int_{t_{n-1}}^{t_n} (V_c + V_1) dt = V_3 \quad \text{or} \quad k_i (V_c + V_1) = \frac{V_3}{t_n - t_{n-1}} = V_3 \text{ p.f.}$$

①

- In Voltage controlled Oscillator, a ramp circuit is generated.
- When $V_c = 0$, the difference between the control pulses is 60° .
- So corresponding to every ramp, we have a control pulse.
- If there is error in the system, V_c will have some value & hence increases beyond $V_c = 0$. [ie
- So in order to delay the control pulse, the value of ' V_c ' should be greater.

- At steady state, $V_c = 0$ and thus

$$K_I = \frac{V_3 p f_0}{V_1}$$

if $V_1 = 0$, K_I has to be very high & that causes instability.
 \rightarrow gain

- V_1 is required?

- Frequency change is not taken care of, hence with V_3 reset, f_0 is also be reset.

- Answorth suggested frequency correction control as

$$K_I = \int_{t_{n-1}}^{t_n} V_1 dt = \frac{V_3 + V_c}{V_1} \quad \text{or} \quad K_I = \frac{V_3 p f_0}{V_1}$$

— from ①

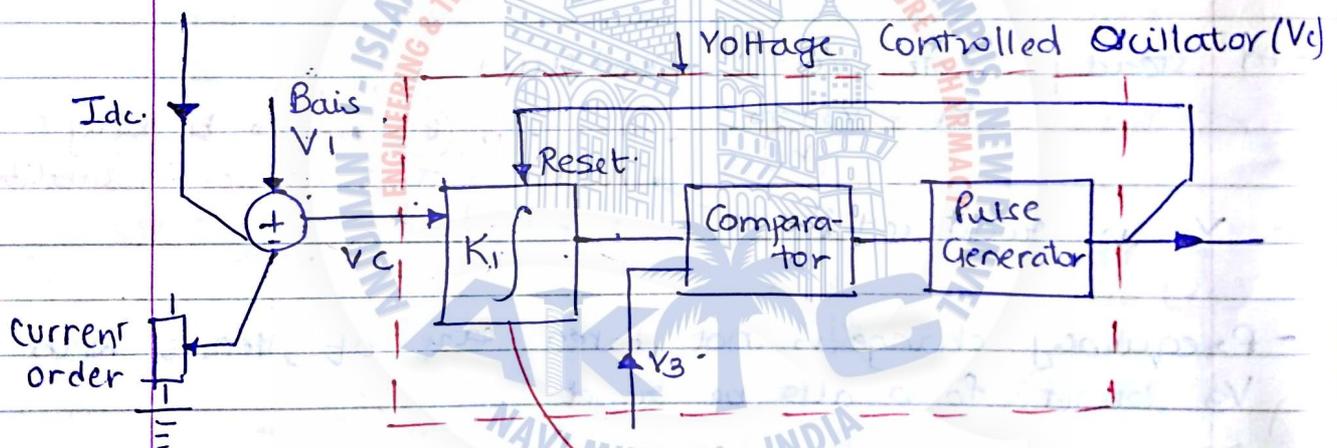
- PFC has better stability but problem of harmonic as control (V_c) is not integrated

Page No.

Date: / /

• Pulse Phase Control (PPC)

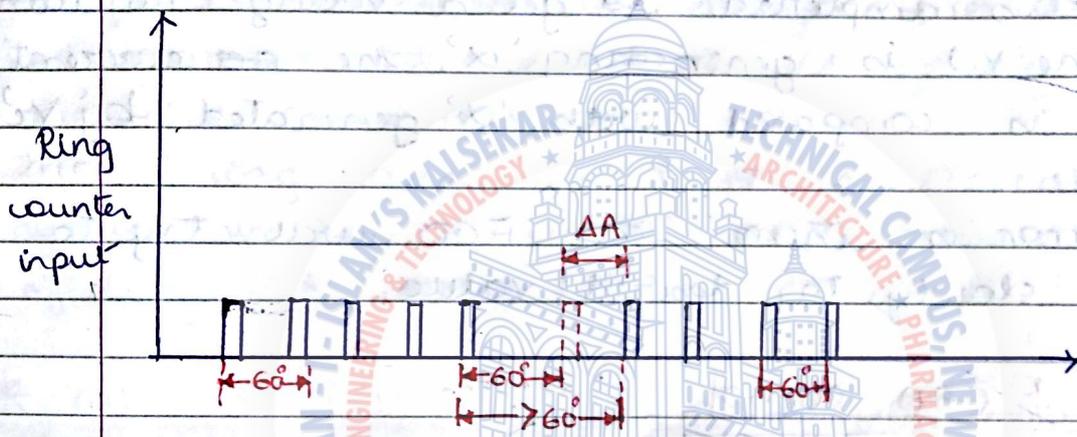
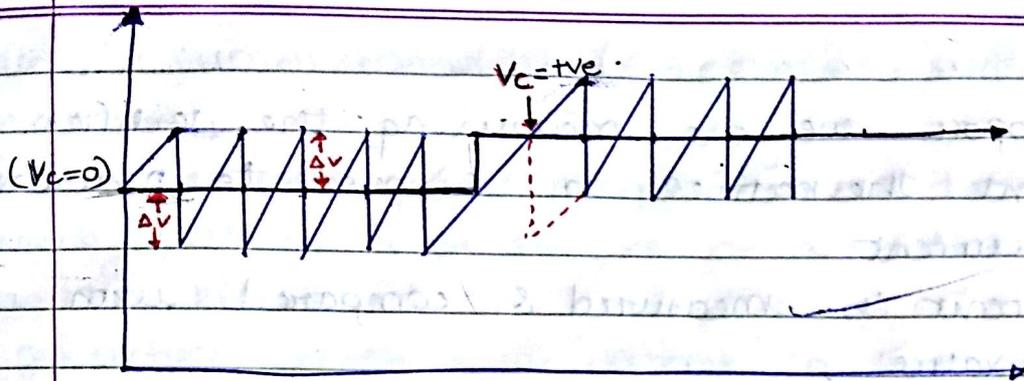
- A train of pulses are generated proportional to the control voltage (V_c).
- Response of this system is fast as it does not have integrator characteristics.
- The charging & discharging of capacitor is maintained between $\pm \Delta V$.



→ this integrator is only to generate ramp (ie. charge & discharge of capacitor)

for this we use a capacitor

- In this method we are not integrating V_c , V_1 so it is faster.



Constant Current Control (CCC) [Rectifier End]

