OLD COLLDS	Code	. 44954
(OLD COURS	Code	: 14354

(3  H)	Hours)	[Total	Marks	:	100

N.	В.:	<ol> <li>Question No. 1 is compulsory.</li> <li>Attempt any four questions out of remaining six questions.</li> <li>Assumptions made should be clearly stated.</li> <li>Assume any suitable data wherever required but justify the same.</li> <li>Figures to the right indicate marks.</li> <li>Illustrate answer with sketches wherever required.</li> <li>Answers to questions should be grouped and written together.</li> <li>Use a bluelblack pen to write answers. Use of pencil should be done only to draw sketches and graphs.</li> </ol>	
1.	(a) (b) (c) (d)	Explain the log Amplifier.	5 5 5 5
2.	(a) (b) (c)		5
3.	(a) (b)	Design second order KRC highpass filter with cut off frequency FO = IKHZ and 12 Q = 5 and draw circuit diagram.  Explan filter approximations.	2
4.	(a) (b)	Explain Sample and Hold circuit .Draw input and output waveform.  Draw the block diagram of IC 565 PLL. Explain in detail FSK demodulation using PLL.	
5.	(a) (b)	Write the VHDL code for 8 bit shift right register.  Draw and explain block diagram of CPLD  10	
6.	(a) (b)	Design Astable Multivibrator using 555 with output frequency 10 KHZ and duty cycle 70%.  Explain inverting Schmitt trigger and find the expression for the hysteresis width 10 for it also mention transfer characteristics.	
7.	Wri (a) (b) (c)	e short notes on :— Function generator IC 8038 V to I convertor using grounded load Comparator circuit.	)