

- Question number 1 is compulsory
- Solve any four out of remaining

- Q1 A Explain functions of interrupt Pins of microprocessor 8085. (5 marks)
- Q1 B. Explain Control Word of 8254 Timer. Write control word for Counter 1, Mode-3, R/W MSB, binary counter. (5 marks)
- Q1 C. Write features of 80386 microprocessor. (5 marks)
- Q1. D. Explain features of coprocessor 8087. (5 marks)
- Q2. A. Draw and Explain Architecture of 8085 Microprocessor. (10 marks)
- Q2 B. Explain modes of PPI 8255. (10 marks)
- Q3. A. Draw and explain interfacing of 8086 in maximum mode with 8259 in cascade mode. (10 marks)
- Q3 B Explain maximum mode of 8086 microprocessor. Draw timing diagram for write Operation in maximum mode of 8086 and explain it. (10 marks)
- Q4 A. Draw and explain interfacing of DAC 0808 with 8086 using 8255. Write a program to generate square wave. (10 marks)
- Q4 B. Draw and interface diagram of 8086 microprocessor and 8087 NDP, also explain various interface signals and co-processor working with host processor. (10 marks)
- Q5. A. Design 8086 microprocessor based system using minimum mode with following specifications:-
- 8086 microprocessor working at 8 MHz
  - 16 KB EPROM using 8 K devices
  - 16 KB SRAM using 8 K devices
- Clearly show memory map with address ranges. Draw a neat Schematic. (10 marks)
- Q5. B. Which are the different types of interrupt supported by 8086? Explain interrupt vector table of 8086. (10 marks)
- Q6. A Write a Program for 8086 microprocessor to exchange memory block of 10 bytes from location 30000 to 40000 (10 marks)
- Q6. B. Draw and explain an architecture of 80286 processor. (10 marks)

Course : S.E (ELECTRONICS & TELECOMMUNICATION) (SEM IV)  
(CBSGS)

Q.P Code : 12485

Correction :

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**CORRECTION:**

1. Question No 1 is compulsory
2. Solve any three out of remaining

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~~Section~~ ETC 4011  
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