

ANJUMAN-I-ISLAM'S KALSEKAR TECHNICAL CAMPUS, NEW PANVEL

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□ SCHOOL OF ENGINEERING & TECHNOLOGY

☐ SCHOOL OF PHARMACY
☐ SCHOOL OF ARCHITECTURE

DEPARTMENT OF ELECTRICAL ENGINEERING

ELECTRICAL & ELECTRONIC MEASUREMENTS

QUESTION BANK

MODULE 4: Measurement of Inductance & Capacitance

- Q1) Explain the construction & working of Maxwell Inductance Capacitance Bridge? Also derive the equation for unknown inductance draw the Phasor diagram
- Q2) Explain Anderson's bridge for measuring self inductance. Draw a neat circuit diagram and Phasor diagram. Derive expression for self inductance under balance condition. State the advantage & disadvantage
- Q3) Explain De Sauty's bridge with circuit diagram, Phasor diagram, derive the expression for measuring the capacitance of a capacitor and also write in detail how the bridge can be modified to measure dielectric loss of a capacitor.
- Q4) Describe the working of a low voltage Schering bridge. Derive the equation for capacitance & dissipation factor
- Q5) Justify Hay's bridge is suitable for measuring inductance of high Q coils. Draw its circuit diagram & Phasor diagram
- Q6) Short note on basic Q meter
- Q7) Explain different types of detector used in AC Bridge