

KALSEKAR TECHNICAL CAMPUS, NEW PANVEL

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Recognised by : Directorate of Technical Education, Govt. of Maharashtra, Affiliated to : University of Mumbai.

SCHOOL OF ENGINEERING & TECHNOLOGY

SCHOOL OF PHARMACY

SCHOOL OF ARCHITECTURE

DEPARTMENT OF ELECTRICAL ENGINEERING

. [EV:00 QUESTION PAPER CLASS TEST 01 EXM		1-04 B		
CLA	ASS:-BE	SEM:-VII			
CO	OURȘE:-PSOC	DATE:27/ 08	DATE:27/08/2018		
DU	DURATION 60 min. MARKS 20			15	
Q.0	01 Attempt any two: (08 Marks)		Marks	СО	
a)	Draw and explain in/out curve and increment rate curve.		04	1	
b)	b) Explain are the types of buses and the need of slack bus in load flow studies.		04	3	
c)	c) Derive the equation of real and reactive power of load flow studies.		04	1	
	Q.02 Attempt any two: (1	2 Marks)			
a)	Derive the expression for the Coordinate Equation.		06	3	
b)	Compare GS ,NR and FDL method in load flow studies.		06	2	
c)	In a two bus system, bus no.1 is a slack bus with $V_1 = 1$ pu & bus power of $S_2=2.8+j0.6$. The transmission line is connected between		06	2	
	Z=0.02+j0.04. By using Guass Siedel method find V ₂ at the end of on				

CRITERION: 2.2.2, 3.2.2.

FILE NO: P25, P31

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R	REV:00	QUESTION PAPER CLASS TEST 01 EX	EXM-04 B		
CL	ASS:-BE	SEM:-VII			
CO	URSE:- H	IVDCT DATE:- 27	/08/18		
DU	RATION:	- 60 min. MARKS:- 20	MARKS:- 20		
			5.		
Q.0	1 Attemp	t any TWO: (08 Marks)	Marks	CO	
a)	"Convert	er consumes reactive power" justify the statement.	04	CO2	
b) What are the limitations of AC transmission and advantages of HVDC transmission?		04	CO1		
c)				CO1	
Q.0	2 Attempt	t any TWO: (12 Marks)			
a)		l explain complete equivalent circuit of HVDC link.	06	CO2	
b)	Derive the output voltage DC equation of a 6 pulse converter operating with delay angle α and overlap angle μ . When (μ <60)		06	CO2	
c)	Explain t	ypes of HVDC links. State advantages and disadvantages of ground return.	06	CO1	

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RE	V:00	QUESTION PAPER CLASS TEST 01	EXM-04 B		
CLASS	S:-BE		SEM:-VII		
COUF	RSE:- EEC70 4		DATE:-28/ 08/ 2018		
DURA	ATION:- 60	min.	MARKS:- 20		
Q.01	Attempt an	y 2: (8 Marks)		Marks	СО
a)	Explain Rev	verse Acting Controller		4	2
b)	Explain integral windup effect and antiwindup circuit.		4	2	
c)	Explain ban	dwidth limit derivative		4	2
Q.02	Attempt an	y 1: (10 Marks)			
		rivative kick and Proportional Kick in detail		12	2
b)		ad compensator such that the closed loop system will sat ans. Static velocity error constant =20 sec, phase marging (SCS+1)		12	1

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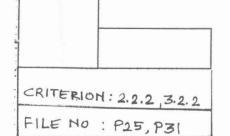
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RE	OO QUESTION PAPER CLASS TEST 01 EXM-04 B		
CLAS	SS:-BE SE	EM:-VII	AMERICAN AMERICAN
COU	RSE:EMD D	ATE:- 28/08/20	18
DUR	ATION:- 60 min.	ARKS:- 20	
Q.0:	L Attempt any ONE. (08 Marks)	Marks	СО
	Discuss design of core of transformer.	8	CO2
b)	Derive output equation of 3ph transformer.	8	CO2
Q.02	2 Attempt any TWO (12 Marks)		
a)	Explain classification of insulating material based on temperature.	6	Co1
b)	Show that for two stepped core Agi=0.56d2 or Ai=0.6626d2.	6	Co2
(C)	Show that $Et=K\sqrt{Q}$	6	Co2



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RE	EV:00 QUESTION PAPER CLASS TEST 01	E	EXM-04 B		
CLAS	SS:- BE	SEM:- VI	Π		
COURSE:- HVE DATE:- 25				9/08/ 2018	
DURATION:- 60 min. MARKS:-					
Q.01	Attempt any ONE: (08 Marks)		Marks	СО	
a) Explain Thermal Breakdown In Solid Di-Electric.			08	1	
b)	b) What Is Electric Field Stress And Explain Uniform And Non- Uniform Fileds.			1	
c) What Is Treeing And Tracking?			08	I	
Q.02	Attempt Any One: (12 Marks)				
,	What Is Intrinsic Strength Of Solid Di-Electric? Explain In Detail Solid.	Breakdown Of	12	1	
b)	Expalin All Three Theories Explaining Breakdown Of Liquid Di-Electric		12	1	
c)	Write in Brief about FE	M	12	1	

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